Optical Transceivers

FTLF1429P3BCV

16GFC RoHS Compliant Long-Wavelength SFP+ Transceiver

FTLF1429P3BCV SFP+ transceivers are designed for use in Fibre Channel links up to 14.025 Gb/s data rate and up to 10 km link length. They are compliant with FC-PI-5 Rev. 6.101 and SFF-8472 Rev 11.03 and SFF-80817. They are compatible with SFF-8432[2] and applicable portions of SFF-8431 Rev. 4.14. The optical transceivers are compliant per the RoHS Directive 2011/65/EU. See Finisar Application Note AN-2038 for more details.

FEATURES

- Up to 14.025 Gb/s bi-directionaldata links
- Hot-pluggable SFP+ footprint
- Built-in digital diagnostic functions
- 1310nm DFB laser transmitter
- Duplex LC connector
- RoHS compliant and Lead Free
- Up to 10 km on 9/125µm SMF
- Metal enclosure, for lower EMI
- 1W maximum power consumptionwith established link
- Single 3.3V power supply
- Extended operating temperaturerange: 0°C to 70°C

APPLICATIONS

- Tri Rate 4.25 / 8.5 / 14.025 Gb/sFibre Channel through Rate Select
- 10GBASE-LR/LW 10G Ethernet

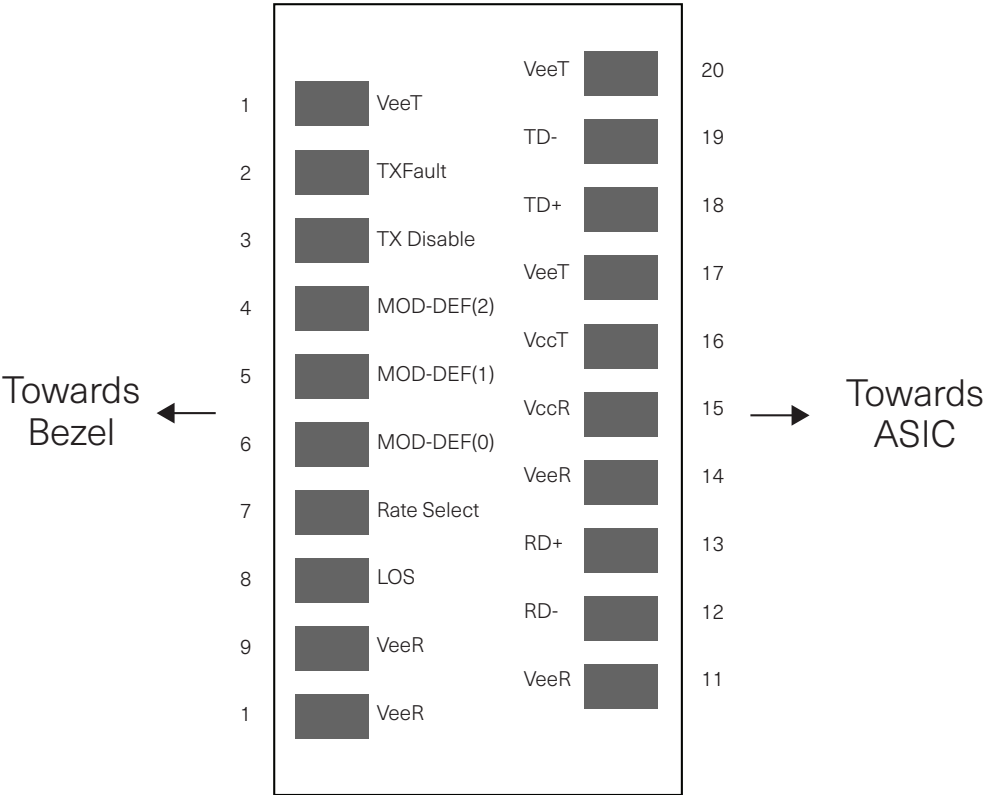**C⊛HERENT**

Product Selection

FTLF1429P3BCV

I. Pin Descriptions

Pin	Name		Pin	Name
1	VeeT		20	VeeT
2	TXFault		19	TD-
3	TX Disable		18	TD+
4	MOD-DEF(2)		17	VeeT
5	MOD-DEF(1)		16	VccT
6	MOD-DEF(0)		15	VccR
7	Rate Select		14	VeeR
8	LOS		13	RD+
9	VeeR		12	RD-
1	VeeR		11	VeeR

Towards Bezel ←

Towards ASIC →

Diagram of Host Board Connector Block Pin Numbers and Names

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Pin	Symbol	Name/Description	Notes
1	V_{EET}	Transmitter Ground (Common with Receiver Ground)	1
2	T_{FAULT}	Transmitter Fault.	2
3	T_{DIS}	Transmitter Disable. Laser output disabled on high or open.	3
4	SDA	2-wire Serial Interface Data Line (MOD-DEF2)	4
5	SCA	2-wire Serial Interface Clock (MOD-DEF1)	4
6	MOD_ABS	Module Absent, connected to V_{EET} or V_{EER}	4
7	RS0	Rx Rate Select: Open or Low = 4.25 or 8.5 Gb/s Fibre Channel (Low Bandwidth) High = 14.025 Gb/s Fibre Channel (High Bandwidth)	5
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	6
9	RS1	Tx Rate Select: Open or Low = 4.25 or 8.5 Gb/s Fibre Channel (Low Bandwidth) High = 14.025 Gb/s Fibre Channel (High Bandwidth)	5
10	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1
11	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1
12	RD-	Receiver Inverted DATA out. AC Coupled	
13	RD+	Receiver Non-inverted DATA out. AC Coupled	
14	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1
15	V_{CCR}	Receiver Power Supply	
16	V_{CCT}	Transmitter Power Supply	
17	V_{EET}	Transmitter Ground (Common with Receiver Ground)	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	V_{EET}	Transmitter Ground (Common with Receiver Ground)	1

Notes
1. Circuit ground is internally isolated from chassis ground.
2. TFAULT is an open collector/drain output, which should be pulled up with a 4.7k – 10k Ohms resistor on the host board if intended for use. Pull up voltage should be between 2.0V to Vcc + 0.3V. A high output indicates a transmitter fault caused by either the TX bias current or the TX output power exceeding the preset alarm thresholds. A low output indicates normal operation. In the low state, the output is pulled to <0.8V.
3. Laser output disabled on T_{DIS} >2.0V or open, enabled on T_{DIS} <0.8V.
4. Should be pulled up with 4.7k – 10kohms on host board to a voltage between 2.0V and 3.6V. MOD_ABS pulls line low to indicate module is plugged in.
5. Rate select can also be set through the 2-wire bus in accordance with SFF-8472 v. 10.2. Rx Rate Select is set at Bit 3, Byte 110, Address A2h. Tx Rate Select is set at Bit 3, Byte 118, Address A2h. Note: writing a "1" selects maximum bandwidth operation. Rate select is the logic OR of the input state of Rate Select Pin and 2-wire bus.
6. LOS is open collector output. Should be pulled up with 4.7k – 10kohms on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

II. Absolute Maximum Ratings

Parameter	Symbol	Min	Typ	Max	Unit	Ref.
Maximum Supply Voltage	Vcc	-0.5		4.2	V	
Storage Temperature	T_S	-40		85	°C	
Case Operating Temperature	T_{OP}	0		70	°C	
Relative Humidity		0		85	%	1

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III. Electrical Characteristics (T_{OP} = -5 to 85 °C, V_{CC} = 3.00 to 3.60 Volts)

Parameter	Symbol	Min	Typ	Max	Unit	Ref.
Supply Voltage	Vcc	3.15		3.45	V	2
Supply Current	Icc		200	300	mA	2
Transmitter						
Input differential impedance	R_{in}		100		Ω	3
Single ended data input swing	Vin,pp	90		350	mV	
Transmit Disable Voltage	V_D	2		VccT+0.3	V	
Transmit Enable Voltage	V_{EN}	Vee		Vee+ 0.8	V	4
Receiver						
Single ended data output swing	Vout,pp	185		425	mV	5
Data output rise/fall time, 4.25 Gb/s	t_r, t_f			120	ps	6
LOS Fault	$V_{LOS\ fault}$	2		Vcc_{HOST}	V	7
LOS Normal	$V_{LOS\ norm}$	Vee-0.3		Vee+0.4	V	7
Power Supply Rejection	PSR	66			mVpp	8

Notes:
1. Non-condensing.
2. Module power consumption never exceeds 1W with established link.
3. AC coupled.
4. Or open circuit.
5. Into 100 ohm differential termination.
6. 20 – 80 %.
7. LOS is LVTTL. Logic 0 indicates normal operation; logic 1 indicates no signal detected.
8. All transceiver specifications are compliant with a power supply sinusoidal modulation of 20 Hz to 1.5 MHz up to specified value applied through the power supply filtering network shown on page 23 of the Small Form-factor Pluggable (SFP) Transceiver MultiSource Agreement (MSA)6, September 14, 2000. The Power Supply Rejection applies for a supply voltage range of 3.1 to 3.6 V.
9. Measured with DJ-free data input signal. In actual application, output DJ will be the sum of input DJ and Δ DJ.
10. For 14.025 and 8.5 Gb/s operation, Deterministic Jitter and Total Jitter are not specified per FC-PI-5 Rev 6.10. Jitter values for gamma T and gamma R are controlled by TDP and stressed receiver sensitivity.

IV. Optical Characteristics (T$_{OP}$ = 0 to 70 °C , V$_{CC}$ = 3.15 to 3.45 Volts)

Parameter	Symbol	Min	Typ	Max	Unit	Ref.
Transmitter						
Output Opt. Power, 14.025 Gb/s	PO,RH	-5.0		+2.0	dBm	1,2
Output Opt. Power, 4.25, 8.5 Gb/s	PO,RL	-5.0		-1.0	dBm	3
Optical Wavelength	λ	1295		1325	nm	4
Spectral Width	δ			1	nm	4,5
Optical Modulation Amplitude, 14.025 Gb/s	OMA	631			µW	6
Optical Modulation Amplitude, 4.25, 8.5 Gb/s	OMA	290			µW	4,6
Transmitter and Dispersion Penalty, 14.025 Gb/s	TDP			4.4	dB	7
Transmitter and Dispersion Penalty, 8.5 Gb/s	TDP			3.2	dB	7
Optical Rise/Fall Time, 4.25 Gb/s	t$_r$/ t$_f$			90	ps	8
RIN				-130	dB/Hz	
Receiver						
Unstressed Receiver OMA Sensitivity, 14.025 Gb/s	R$_{SENSr}$			0.063	mW	
Unstressed Receiver OMA Sensitivity, 8.5 Gb/s	R$_{SENSr}$			0.042	mW	9
Unstressed Receiver OMA Sensitivity, 4.25 Gb/s	R$_{SENS4}$			0.029	mW	9
Average Received Power	Rx$_{MAX}$			+2.0	dBm	
Optical Center Wavelength	λ$_C$	1260		1370	nm	
Return Loss		12			dB	
LOS De-Assert	LOS$_D$			-19	dBm	
LOS Assert	LOS$_A$	-30			dBm	
LOS Hysteresis		0.5			dB	

Notes:
1. Class 1 Laser Safety per FDA/CDRH and EN (IEC) 60825 regulations.
2. High Bandwidth Mode. Class 1 Laser Safety per FDA/CDRH and EN (IEC) 60825 regulations.
3. Low Bandwidth Mode. Class 1 Laser Safety per FDA/CDRH and EN (IEC) 60825 regulations.
4. Also specified to meet curves in FC-PI-5 Rev 6.10[1] Figures 23, 24, and 25, which allow trade-off between wavelength, spectral width and OMA for 4.25 and 8.5 Gb/s operation.
5. 20dB spectral width.
6. Equivalent extinction ratio specification for Fibre Channel. Allows smaller ER at higher average power.
7. For 14.025 and 8.5 Gb/s operation, Jitter values for gamma T and gamma R are controlled by TDP.
8. Unfiltered, 20-80%. Complies with IEEE 802.3 (Gig. E), FC 4x eye masks when filtered.
9. Measured with PRBS 2[7]-1 at 10[-12] BER.

V. General Specifications

Parameter	Symbol	Min	Typ	Max	Unit	Ref.
Data Rate	BR	4.25		14.025	Gb/sec	1
Bit Error Rate	BER			10^{-12}		2
Supported Link Length on 9/125 µm SMF , 4.25, 8.5, 14.025 Gb/s	L_{MAX1}		10		km	3

Notes:
1. 4x/8x/16x Fibre Channel compliant.
2. Tested with a PRBS 27-1 test pattern for 4.25 and 8.5Gb/s operation. Tested with a PRBS 231-1 test pattern for 14.025Gb/s operation.
3. Distances are based on FC-PI-5 Rev. 6.101 and IEEE 802.3 standards.

VI. Environmental Specifications

These Finisar 1310nm Extended Temperature SFP+ transceivers have an operating temperature range from -5°C to +85°C case temperature.

Environmental Specifications	Symbol	Min	Typ	Max	Units	Ref.
Case Operating Temperature	T_{op}	0		70	°C	
Storage Temperature	T_{sto}	-40		85	°C	

VII. Regulatory Compliance

Finisar transceivers are Class 1 Laser Products and comply with US FDA regulations. These products are certified by TÜV and CSA to meet the Class 1 eye safety requirements of EN (IEC) 60825 and the electrical safety requirements of EN (IEC) 60950. Copies of certificates are available at Finisar Corporation upon request.

VIII. Digital Diagnostic Functions

Finisar FTLF1429P3BCV SFP+ transceivers support the 2-wire serial communication protocol as defined in the SFP MSA[6]. It is very closely related to the E^{2}PROM defined in the GBIC standard, with the same electrical specifications.

The standard SFP serial ID provides access to identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information.

Additionally, Finisar SFP transceivers provide a unique enhanced digital diagnostic monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage. It also defines a sophisticated system of alarm and warning flags, which alerts end-users when particular operating parameters are outside of a factory set normal range.

The SFP MSA defines a 256-byte memory map in E^{2}PROM that is accessible over a 2-wire serial interface at the 8 bit address 1010000X (A0h). The digital diagnostic monitoring interface makes use of the 8 bit address 1010001X (A2h), so the originally defined serial ID memory map remains unchanged. The interface is identical to, and is thus fully backward compatible with both the GBIC Specification and the SFP Multi Source Agreement. The complete interface is described in Finisar Application Note AN- 2030: "Digital Diagnostics Monitoring Interface for SFP Optical Transceivers".

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through a 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL, Mod Def 1) is generated by the host. The positive edge clocks data into the SFP transceiver into those segments of the E^{2}PROM that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

For more information, please see the SFP MSA documentation[3,6] or Finisar Application Note AN-2030.

FTLF1429P3BCV transceivers can be used in host systems that require either internally or externally calibrated digital diagnostics.

Please note that evaluation board FDB-1027 is available with Finisar ModDEMO software that allows simple to use communication over the 2-wire serial interface.
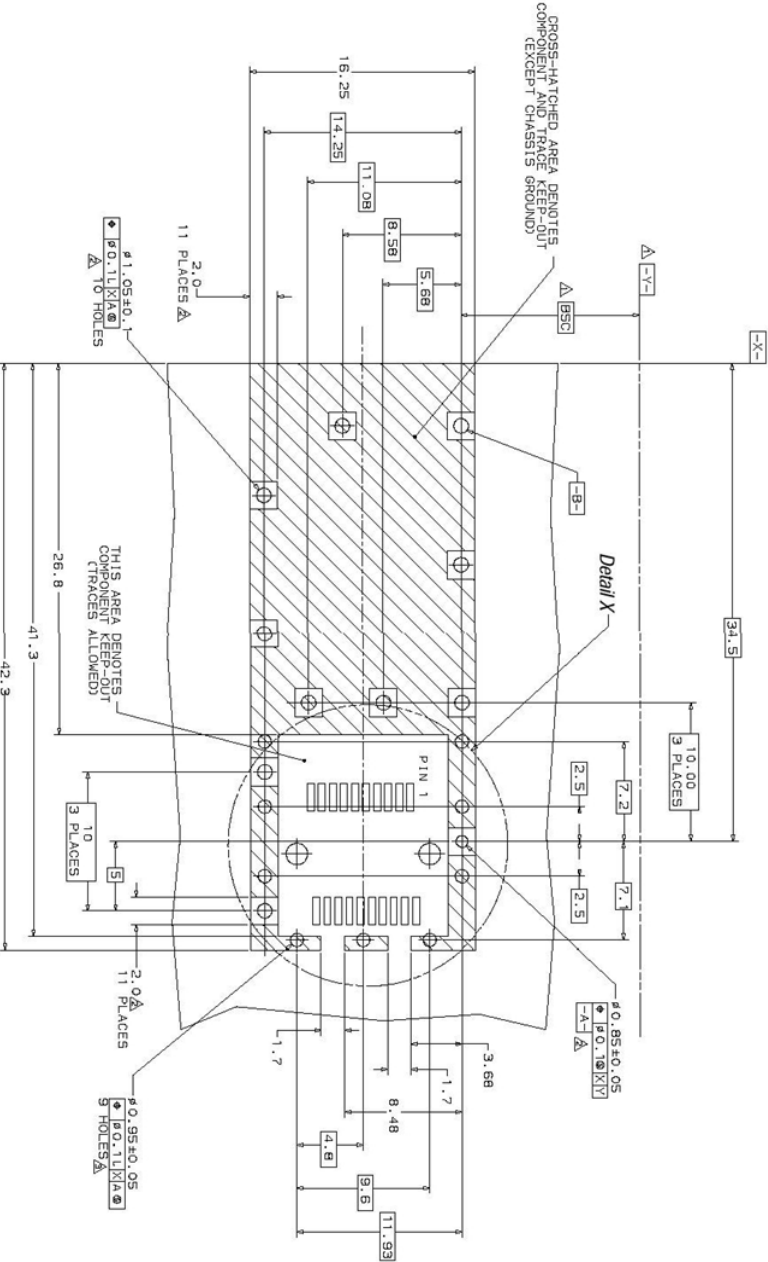
IX. Mechanical Specifications

Finisar's FTLF1429P3BCV SFP+ transceivers are compatible with the SFF-8432[2] specification for improved pluggable form factor.
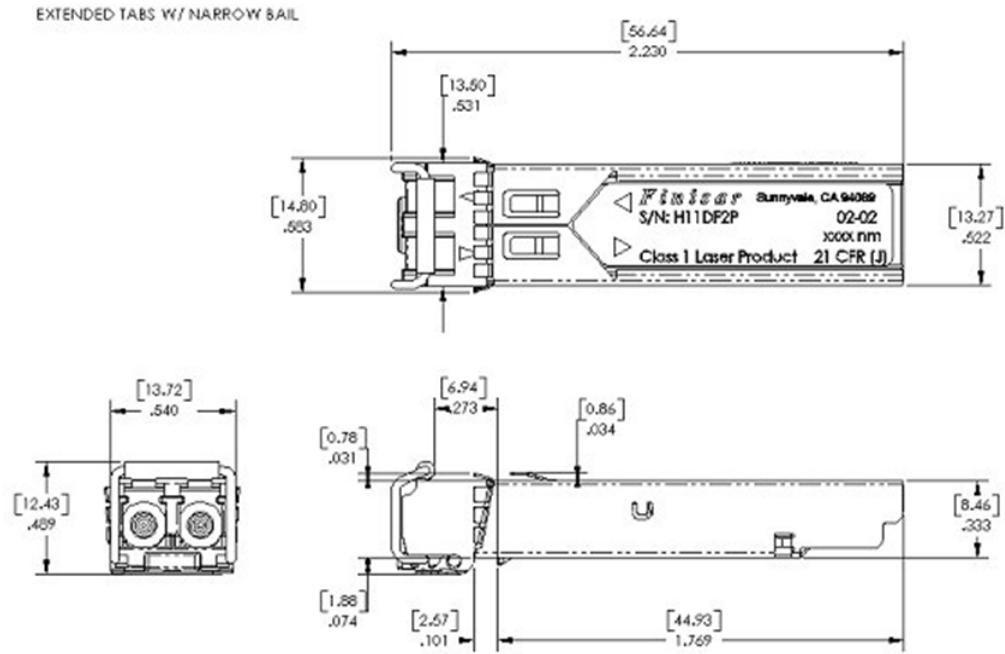
FTLF1429P3BCV

X. PCB Layout and Bezel Recommendations

⚠ Datum and Basic Dimension Established by Customer

⚠ Pads and Vias are Chassis Ground, 11 Places

⚠ Through Holes are Unplated

NOTES:

⚠ MINIMUM PITCH ILLUSTRATED, ENGLISH DIMENSIONS
ARE FOR REFERENCE ONLY

2. NOT RECOMMENDED FOR PCI EXPANSION
CARD APPLICATIONS

XI. References

1. "Fibre Channel – Physical Interfaces -5, INCITS 479-2011 (Defines 4GFC, 8GFC and 16GFC).
2. "Improved Pluggable Formfactor", SFF Document Number SFF-8432, Revision 5.0, July 16, 2007.
3. "Digital Monitoring Interface for Optical Transceivers", SFF Document Number SFF-8472, Revision 11.0.
4. "Enhanced Small Form Factor Pluggable Module 'SFP+ '", SFF Document Number SFF-8431, Revision 4.1, July 6, 2009.
5. Directive 2011/65/EU of the European Council Parliament and of the Council, "on the restriction of the use of certain hazardous substances in electrical and electronic equipment". Certain products may use one or more exemptions as allowed by the Directive.
6. Small Form Factor Pluggable (SFP) Transceiver Multi-source Agreement (MSA), September 14, 2000 which is available as SFF Document Number INF-8074, Revision 1.0 May 12, 2001.
7. "0.8mm Card Edge Connector for 16Gb/s Applications", SFF Document Number SFF-8081, Revision 0.2, December 21, 2009